

**लोक सेवा आयोग**  
संघीय संसद सेवा, प्राविधिक समूह, राजपत्राङ्कित तृतीय श्रेणी, इलेक्ट्रोनिक्स इन्जिनियर पदको खुला  
प्रतियोगितात्मक परीक्षाको पाठ्यक्रम

यस पाठ्यक्रम योजनालाई दुई चरणमा विभाजन गरिएको छ :

प्रथम चरण :-	लिखित परीक्षा (Written Examination)	पूर्णाङ्क :- २००
द्वितीय चरण :-	(क) सामूहिक परीक्षण (Group Test)	पूर्णाङ्क :- १०
	(ख) अन्तर्वार्ता (Interview)	पूर्णाङ्क :- ३०

**परीक्षा योजना (Examination Scheme)**

प्रथम चरण : लिखित परीक्षा (Written Examination)

पूर्णाङ्क :- २००

पत्र	विषय	खण्ड	पूर्णाङ्क	उर्तीर्णाङ्क	परीक्षा प्रणाली		प्रश्नसंख्या × अङ्क	समय
प्रथम	General Subject	Part I: General Awareness & General Ability Test	१००	४०	वस्तुगत (Objective)	बहुवैकल्पिक प्रश्न (MCQs)	५० प्रश्न × १ अङ्क	१ घण्टा ३० मिनेट
		Part II: General Technical Subject					५० प्रश्न × १ अङ्क	
द्वितीय	Technical Subject		१००	४०	विषयगत (Subjective)	छोटो उत्तर लामो उत्तर	४ प्रश्न × ५ अङ्क ८ प्रश्न × १० अङ्क	३ घण्टा

द्वितीय चरण : सामूहिक परीक्षण (Group Test) र अन्तर्वार्ता (Interview)

पूर्णाङ्क :- ४०

पत्र /विषय	पूर्णाङ्क	उर्तीर्णाङ्क	परीक्षा प्रणाली	समय
सामूहिक परीक्षण (Group Test)	१०		सामूहिक छलफल (Group Discussion)	३० मिनेट
अन्तर्वार्ता (Interview)	३०		बोर्ड अन्तर्वार्ता (Board Interview)	-

**द्रष्टव्य :**

- लिखित परीक्षाको माध्यम भाषा नेपाली वा अंग्रेजी अथवा नेपाली र अंग्रेजी दुवै हुनेछ ।
- प्रथमपत्र र द्वितीयपत्रको लिखित परीक्षा छुट्टाछुट्टै हुनेछ ।
- वस्तुगत बहुवैकल्पिक (Multiple Choice) प्रश्नहरूको गलत उत्तर दिएमा प्रत्येक गलत उत्तर बापत २० प्रतिशत अङ्क कट्टा गरिनेछ । तर उत्तर नदिएमा त्यस बापत अङ्क दिइने छैन र अङ्क कट्टा पनि गरिने छैन ।
- बहुवैकल्पिक प्रश्नहरू हुने परीक्षामा कुनै प्रकारको क्याल्कुलेटर (Calculator) प्रयोग गर्न पाइने छैन ।
- विषयगत प्रश्नहरूको हकमा तोकिएको अंकको एउटा लामो प्रश्न वा एउटा प्रश्नका दुई वा दुई भन्दा बढी भाग (Two or more parts of a single question) वा एउटा प्रश्न अन्तर्गत दुई वा बढी टिप्पणीहरू (Short notes) सोध्न सकिने छ ।
- द्वितीय पत्रमा (विषयगत प्रश्न हुनेका हकमा) प्रत्येक खण्डका लागि छुट्टाछुट्टै उत्तरपुस्तिकाहरू हुनेछन् । परिक्षार्थीले प्रत्येक खण्डका प्रश्नहरूको उत्तर सोही खण्डको उत्तरपुस्तिकामा लेख्नुपर्नेछ ।
- यस पाठ्यक्रम योजना अन्तर्गतका पत्र/विषयका विषयवस्तुमा जेसुकै लेखिएको भए तापनि पाठ्यक्रममा परेका कानून, ऐन, नियम तथा नीतिहरू परीक्षाको मिति भन्दा ३ महिना अगाडि (संशोधन भएका वा संशोधन भई हटाईएका वा थप गरी संशोधन भई) कायम रहेकालाई यस पाठ्यक्रममा परेको सम्भन्नु पर्दछ ।
- प्रथम चरणको परीक्षाबाट छनौट भएका उम्मेदवारहरूलाई मात्र द्वितीय चरणको परीक्षामा सम्मिलित गराइनेछ ।
- यस भन्दा अगाडि लागू भएका माथि उल्लेखित सेवा, समूहको पाठ्यक्रम खारेज गरिएको छ ।
- पाठ्यक्रम लागू मिति : - २०७६/०७/२५

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प्रथम पत्र (Paper I): General Subject

**Part (I) : - General Awareness & General Ability Test (50 Marks)**

1. **General Awareness and Contemporary Issues (25 ×1 Mark = 25 Marks)**
  - 1.1 Physical, socio-cultural and economic geography and demography of Nepal
  - 1.2 Major natural resources of Nepal
  - 1.3 Geographical diversity, climatic conditions, and livelihood & lifestyle of people
  - 1.4 Notable events and personalities, social, cultural and economic conditions in modern history of Nepal
  - 1.5 Current periodical plan of Nepal
  - 1.6 Information on sustainable development, environment, pollution, climate change, biodiversity, science and technology
  - 1.7 Nepal's international affairs and general information on the UNO, SAARC & BIMSTEC
  - 1.8 The Constitution of Nepal (From Part 1 to 5 and Schedules)
  - 1.9 Governance system and Government (Federal, Provincial and Local)
  - 1.10 Provisions of civil service act and regulation relating to constitution of civil service, organisational structure, posts of service, fulfillment of vacancy and code of conduct
  - 1.11 Functional scope of public services
  - 1.12 Public Service Charter
  - 1.13 Concept, objective and importance of public policy
  - 1.14 Fundamentals of management : planning, organizing, directing, controlling, coordinating, decision making, motivation and leadership
  - 1.15 Government planning, budgeting and accounting system
  - 1.16 Major events and current affairs of national and international importance
2. **General Ability Test (25 ×1 Mark = 25 Marks)**
  - 2.1 **Verbal Ability Test (8×1 Mark = 8 Marks)**

Jumble words, Series, Analogy, Classification, Coding-Decoding, Matrix, Ranking Order Test, Direction and Distance Sense Test, Common Sense Test, Logical Reasoning, Assertion and Reason, Statement and Conclusions
  - 2.2 **Numerical Ability Test (9×1 Mark = 9Marks)**

Series, Analogy, Classification, Coding, Arithmetical reasoning/operation, Percentage, Ratio, Average, Loss & Profit, Time & Work, Data interpretation & Data verification
  - 2.3 **Non-verbal/Abstract Ability Test (8×1 Mark = 8 Marks)**

Figure Series, Figure Analogy, Figure Classification, Figure Matrix, Pattern Completion/Finding, Analytical Reasoning Test, Figure Formation and Analysis, Rule Detection, Water images, Mirror images, Cubes and Dice & Venn-diagram

**Part (II) : - General Technical Subject (50 Marks)**

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- 1. Electronic Device and Circuit** **10%**

Diodes (Tunnel, varactor, zener, diac, Triac, bridge, Impatt, Gunn, photo) and applications, Bipolar transistors switching characteristics, unijunction transistor, MOS transistors switching characteristics, SCR, UJT, TTL logic circuits, NMOS/CMOS logic circuits, memory: RAM, DRAM, PROM, EPROM, operational amplifiers, Butterworth and Chebysev filters, A/D converters, adders, arithmetic operations, digital comparators, parity check generator, multiplexer and demultiplexer, flip-flops, shift register, counters, sequence generators, oscillators(wien bridge oscillators, tuned, LC oscillators, crystal, clap modification), resosant circuits, thyristor, controlled rectifier circuits, 7 segment display, amplifier (Untuned, push-pull, feed back amplifiers, Klystron, Magetrons) bode plot analysis, Emitter, clipper, collector, clamper circuits
- 2. Communication Engineering** **14%**

Frequency spectrum (discrete and continuous) and bands, coulomb's law and electric field intensity, electric flux density and gauss' law, Spectral density, Noises (atmospheric, thermal, partition, white noise, Gaussian noise, Noise ratio(s)), Maxwell's first equation and application, divergence theorem, energy and potential, Fourier series, Laplace equation and Poisson equation, biot-svart's law, ampere's circuital law, curl, wave motion in free space, perfect dielectric and losses, wave medium, skin effect, impedance matching, antenna fundamental (Horn, slot, parapolic, yagi, Cassegrain, lens), polarization, radiation from dipole antenna, wave guides(broad-band guides, Cylindrical, ridged)
- 3. Instrumentation & Control System** **10%**

Dynamometer, Multimeter, Oscilloscopes, Signal generator, Impedance Bridges (Maxwell, Hay, Schering, Anderson, Desauty), Transducers (Strain Guages, Thermistor, Piezoelectric tacheometer, thermocoupler) Open loop and closed loop control system, system stability and sensitivity, system transfer functions and responses, poles and zeros location and their significance, root locus method, frequency response method, Bolometers technique for measurement of power
- 4. Signal analysis and processing** **6%**

Discrete probability theory, Information theory, Shannon-Hartley law, transmission of signal, impulse response and convolution, Fourier series, Fourier transform, unit step, delta, sinc and signum function, helbert transform, LTI system, system described by differential and difference equations, FIR and IIR filters, discrete Fourier transforms, IDFT, FFT, circular convolutions, Parseval's theorem, energy, power and autocorrelation, Z transform
- 5. Basic Analog and Digital Communications** **10%**

Difference between analog and digital communications, basic communication elements, signal and noise in communication system, AM, DSC-SC, PM, FM, Super-heterodyne AM and FM receiver, SSB, D/A and A/D Converters, sampling theorem, sample and hold circuit, A law, m-law, quantizer, coding (NRZ/HDB3/AMI), error detection and correction, Parity check, PCM/ADPCM, digital modulation (ASK/PSK/QPSK/MSK/QAM/CDMA/FDMA/DSSS), pulse modulation, modulation and demodulation circuits, Frequency hopping, frequency converter and mixers, phase locked loop
- 6. Telecommunications and advanced communications** **14%**

Different between Telecommunications and Radio Communication, return loss transformer and hybrid circuit, signal and noise measurements, echo and singing, space/time/frequency /wave length division multiplexing, erlang B formulae, queuing theorem, OSI layers, telephony, functions of switching, electro mechanical switches, stored programmed controlled switch (TS/ST/TST/STS switching, no. 5 and no. 7 control signaling), general concept of

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ISDN, BISDN, ATM, PDH/SDH, DSL, HDSL, ADSL, numbering, routing and channeling plans; UMTS, IMT-2000, NGN (Next Generation Network), real time protocol, VoIP, IP/PSTN platform, Overview to IN (Intelligent Network), Basics of GIS (Geographical Information System)

- 7. Optical Sytem 6%**  
Laser, Photocell, photo device (LED, CRT, photovoltaic, photo-multipliers, APD's PCN), Principle of optical communication, Total internal reflection, Snell's law, Chemical vapour Deposition, optical fibers types, capacity and properties, optical transmission, optical transmitters and receivers, interconnected and switched, Joining techniques, splices, connectors and coupling, fiber optics networks, optical switching, underground cabling (Route and ambient consideration, tension prediction)
- 8. Wireless Communications 10%**  
Radio frequency band, Propagation theory (groundwave, spacewave, tropospheric, ionospheric), Euler-Larmour theory, LOS (line of sight) and non-LOS model, Okumara and Hata model, Mobile Technologies (DECT, GSM, CDMA2000-1x and etc.), Fundamental of satellite communication (tracking, Satellite orbits and Radio spectrum, satellite wave propagation and satellite antennas), digital satellite communication system, earth stations, Kepler's laws of orbital motion, signal to noise ratio, interference between different wireless systems. Antennas (Directional, Non-directional, reflective), impedance and effective length of antenna as transmitter & receiver, Radiation pattern, broad-side pattern, Eudfire pattern, Pattern synthesis
- 9. Computers and Network 10%**  
Different between analogue and digital computer, Binary system and Boolean algebra, Gates, Computer structure (I/O devices, Storage devices, Memories) and typical processor architecture, CPU and memory organization, buses, Characteristics of I/O and storage devices, Processing unit and controller design, hardware and micro program control, Instruction sets and addressing modes, memory systems (main, auxiliary, virtual, cache), assembly language programming, I/O and interrupt servicing, Multiplexing, (time, frequency and code division multiplexing), Digital networks: ISDN, frame relay and ATM. Protocols: (such as ISO/OSI reference model, X.25, IP), LAN/WAN topologies, access schemes, medium access and logic layers; CSMA/CD and token ring protocols; segmented and hubbed LANs, Operating system principles, components, and usage(Multitasking and/or multiprocessing, Real-time aspects)
- 10. Information and Communications Technology (ICT) 10%**  
Computer architecture, microprocessor fundamental, microcomputer systems, parallel and serial interfaces, RS-232 standards, flow charts, algorithms, variables, constants, data types, arithmetic expressions, arrays, concept of Operating System, Basic concept on internet, e-mail and web-page (such as DNS, IP, URL, http, ftp, IRQ, Routers). Server (Web, email, printer), General concept of Cyber security (digital signature, SPAM, VIRUS, WORM, hiking, cracking), Unicode